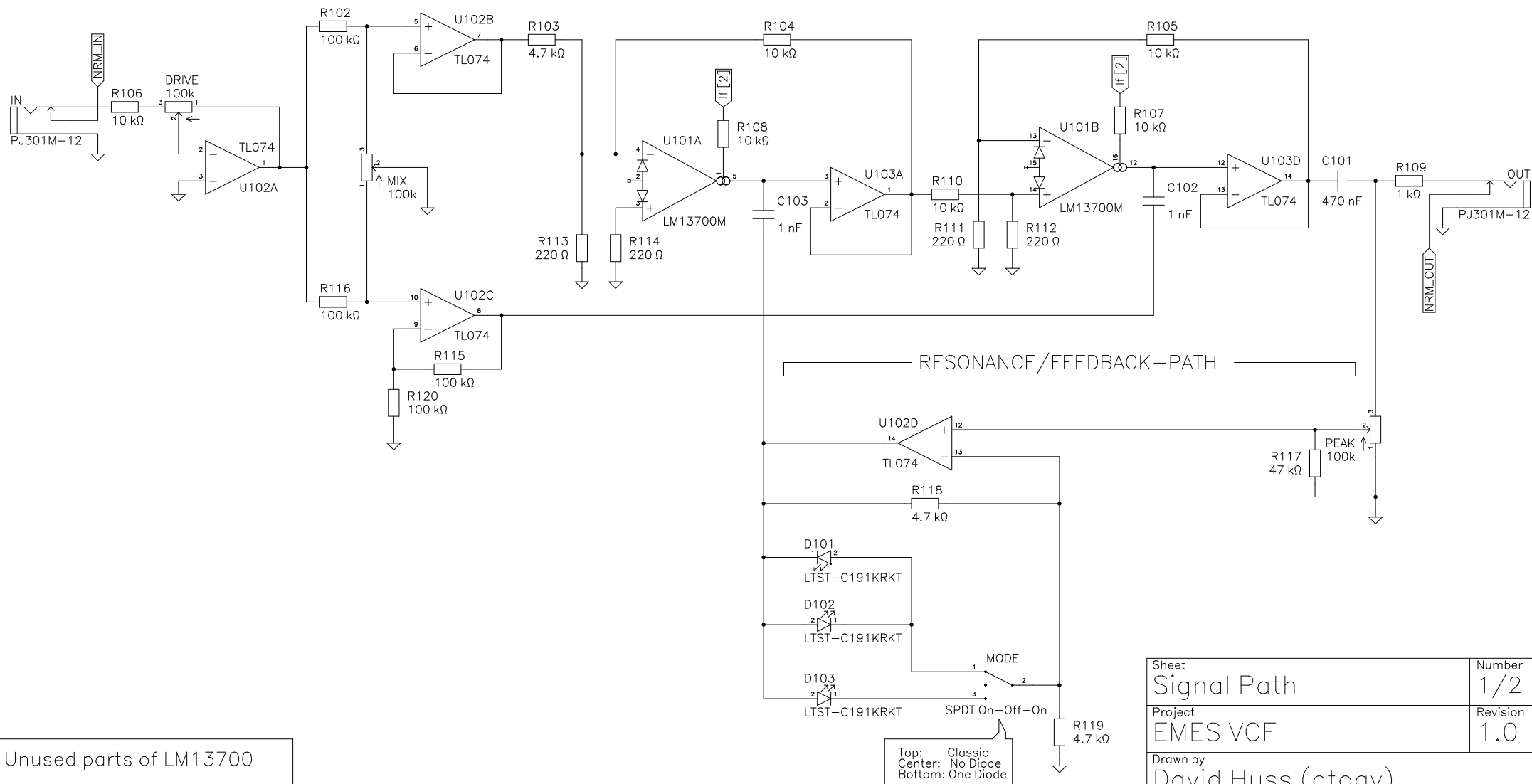
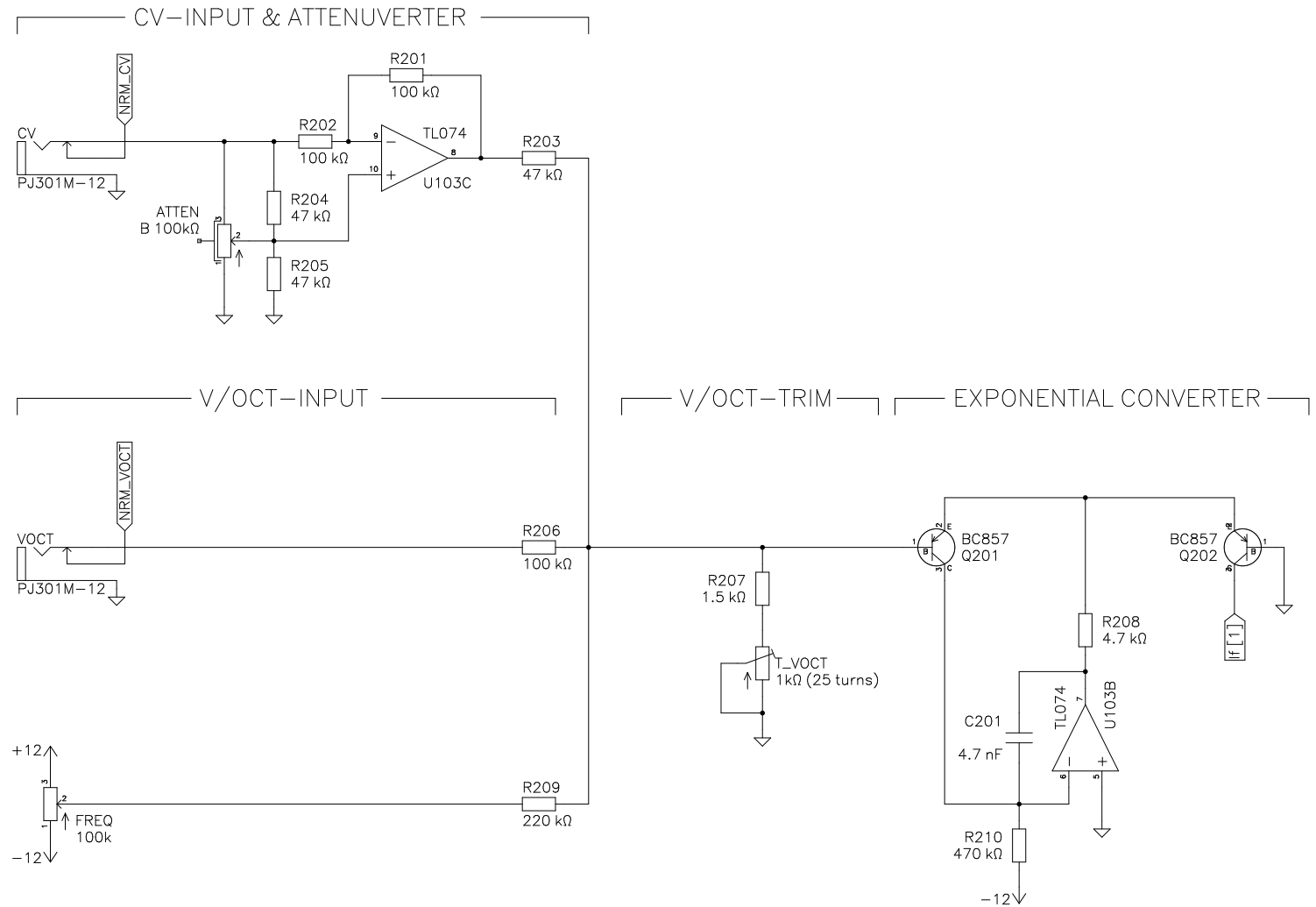


INPUT/DRIVE      HP/LP MIX      STAGE 1      STAGE 2      OUTPUT

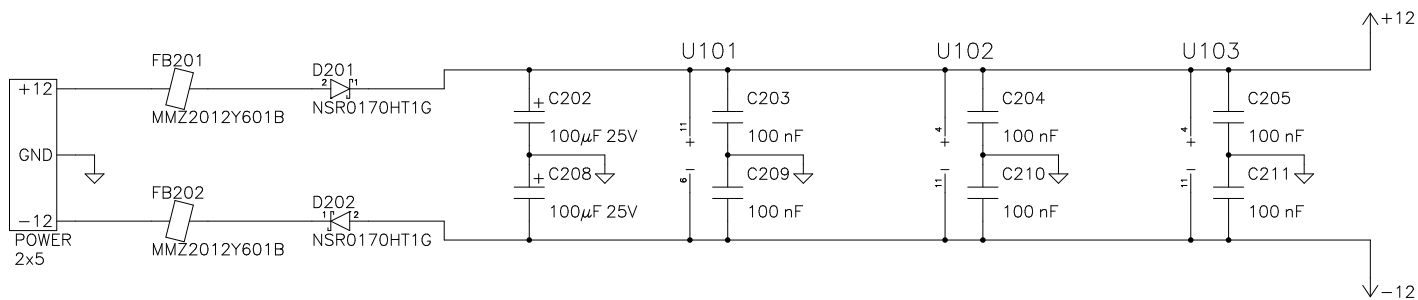



Sheet	Number
Signal Path	1/2
Project	Revision
EMES VCF	1.0
Drawn by	
David Huss (atoav)	
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Date	
2021-08-28	





## POWER SECTION



Sheet	CV & Power	Number	2/2
Project	EMES VCF	Revision	1.0
Drawn by	David Huss (atoav)		
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Date	2021-08-28		
		 open source hardware	